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764164605086

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Yang et al.
Serial No.: 10/683632 Art Unit: Unknown
Filed: October 10, 2003 Examiner: Unknown
Title: Precision Low Jitter Oscillator Circuit

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

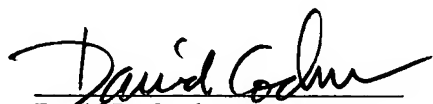
Sir:

Supplemental Information Disclosure Statement

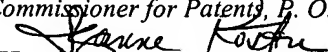
In accordance with the duty of disclosure imposed by 37 C.F.R. § 1.56 and §1.97(e)(1), applicants hereby advise the United States Patent and Trademark Office of certain references which may be material to the determination of patentability of the above-identified application. The references are identified on the attached Form PTO-1449. The item(s) were cited in a counterpart foreign application. Applicants respectfully request that these references be considered and made of record in the present application by completing and returning the enclosed Form PTO-1449.

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Respectfully submitted,


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Date: December 17 2003.

I hereby certify that this correspondence is being deposited with the United States Postal Service via first class mail to: Commissioner for Patents, P. O. Box 1450, Arlington, VA 22313-1450 on the date indicated below.
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PTO/SB/08A (10-01)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/683632	
				Filing Date	10/10/2003	
				First Named Inventor	Yang	
				Art Unit	Unknown	
				Examiner Name	Unknown	
Sheet	1	of	1	Attorney Docket Number	764164605086	
U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Document Number	Publication/Patent Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		Number - Kind Code ² (if known)				
		US-5191301	3/2/1993	Mullgrav, Jr.		
		US-6054885	4/25/2000	Ooishi et al.		
		US-6222423	4/24/2001	Sudjian		
		US-				
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FOREIGN PATENT DOCUMENTS						
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